

TE2012 (Electronics Engineering) Exam 2015  
Microcontrollers and Applications (304203)  
(1st semester - I)

TE/InSem. 133

Solution - SET II

Q.1 a) Each comparison point  $\frac{1}{2}$  Marks.  
Diagram 2 Mark

b) Each selection criteria.  $\frac{1}{2}$  Marks.

c) Each advantage  $\frac{1}{2}$  Marks.

OR

Q.2 a) SPI diagram - 2 Marks.  
Explanation 1 Marks.  
Comparison 2 Marks.

b) Definition of embedded sys 2 Marks.  
Each chara.  $\frac{1}{2}$  Marks.

Q.3. a) Calculation 2 Marks.  
 $T_H, T_L, T_{MOD}$  — 1 Marks each.  
b) block diagram — 3 Marks  
Explanation — 2 Marks.

OR

Q.4 a) Each addressing mode with example 1 Marks  
b) Each Memory organisation. block diag:  $1\frac{1}{2}$  Marks  
1) RAM 1 marks for explanation  
2) ROM 1 marks for explanation.

Q.5 a) Each features  $\frac{1}{2}$  Marks.  
b) Each instruction with example 1 Mark.

Q.6 a) Explanation of each config bit 1 Mark.  
b) Explanation WREG — 2 Mark, Diagram 2 Marks  
STATUS — 2 Marks